EXHIBIT A

For assistance with TSDR, email teas@uspto.gov and include your serial number, the document you are looking for, and a screenshot of any error messages you have received. Processing Wait Times: Please note that due to an extraordinary surge in applications, processing times are longer than usual. See current trademark processing wait times for more information. **Back to Search** Print STATUS DOCUMENTS Generated on: This page was generated by TSDR on 2022-07-28 05:32:24 EDT Mark: TOGETHER WE ADVANCE TOGETHER WE ADVANCE US Serial Number: 90521988 Application Filing Date: Feb. 10, 2021 Register: Principal Mark Type: Trademark, Service Mark **TM5** Common Status LIVE/APPLICATION/Under Examination Descriptor: The trademark application has been accepted by the Office (has met the minimum filing requirements) and that this application has been assigned to an examiner. Status: A non-final Office action has been sent (issued) to the applicant. This is a letter from the examining attorney requiring additional information and/or making an initial refusal. The applicant must respond to this Office action. To view all documents in this file, click on the Trademark Document Retrieval link at the top of this page. Status Date: Mar. 11, 2022 **Mark Information** Mark Literal Elements: TOGETHER WE ADVANCE Standard Character Claim: Yes. The mark consists of standard characters without claim to any particular font style, size, or color. Mark Drawing Type: 4 - STANDARD CHARACTER MARK

Foreign Information

Privacy - Terms

Priority Claimed:	Yes		
Foreign Application Number:	TO/M/2020/03975	Foreign Application Filing Aug. 11, 2020 Date:	
Foreign Application/Registration Country:			
Goods and Services			

Note:

The following symbols indicate that the registrant/owner has amended the goods/services:

- Brackets [..] indicate deleted goods/services;
- Double parenthesis ((..)) identify any goods/services not claimed in a Section 15 affidavit of incontestability; and
- Asterisks *..* identify additional (new) wording in the goods/services.
 - For: Accelerated data processors; accelerated video processors; downloadable audio and video graphics software for real time information and image transfer, transmission, reception, processing and digitizing; computer hardware for enhancing and providing real-time transfer, transmission, reception, processing and digitizing of audio and video graphics information; audio circuit boards; audio-video circuit boards; cards containing integrated circuits; central processing unit (CPU); central processing unit (CPU) coolers; central processing unit (CPU) fans; central processing unit (CPU) heat sinks; central processing units for computers; central processing units for processing information, data, sound and image; graphics processing unit (GPU); blank electronic chip cards; circuit boards with integrated circuits; circuit boards; communication computer; downloadable communication software to facilitate communications among and between persons, machines and apparatus, including computers; computer accelerator board; computer and computer peripheral parts thereof, accessories and electronic components of computers; computer case fans, namely internal cooling fans for computers; computer chips; computer chipsets; computer circuit boards; computer data and image processors; computer daughter boards; computer game cards; computer game programs downloaded via the Internet; computer game software, downloadable; computer game software, recorded; computer graphic chipsets; computer graphics boards; computer hardware for creating, remote access to and communication with local area network (LAN), virtual private network (VPN), and global computer networks; computer hardware for enhancing and providing real-time transfer, transmission, reception, processing and digitizing of audio and video graphics information; computer hardware for the development, and use of local and wide area computer networks; computer hardware subsystems comprised of microprocessor subsystems; microprocessor modules; computer hardware; computer internal cooling fans; computer memories; computer memory devices, namely, volatile memory devices; computer motherboards; computer network server; computer operating software; computer peripheral devices; computer peripherals; downloadable and recorded computer programs for computer, central processing unit (CPU) and graphics processing unit (GPU) operation; computer servers; downloadable and recorded computer software applications for computer, central processing unit (CPU) and graphics processing unit (GPU) operation; downloadable computer software for databasing, visualization, manipulation, virtual reality immersion and integration of information; downloadable computer software for graphics processing and rendering video games; downloadable computer software for machine learning, deep learning, artificial intelligence, and data analysis; downloadable computer software for managing network functions; downloadable computer software for operating microprocessors; computer software platforms, recorded or downloadable, for developing, sharing and accessing open source software, APIs, and graphical software, for analyzing data from a wide variety of sources, data bases and files and of multiple data types for pattern recognition, data mining, data query, and data

analysis, and for software development tools, computer programs for developing and controlling user interfaces, software application programming interfaces, and software for machine learning, data mining, data query, and data analysis; computer software, recorded, for operating computers central processing units (CPU) and graphics processing units (GPU); computer workstations, namely, computers designed for advanced technical or scientific applications and high performance computing applications; computers; coolers for use in electronic components, namely, fans, heat sinks, and liquid coolers; cooling elements for computers, namely, fans, heat sinks and liquid coolers; electronic heat dissipation apparatus, namely, computer heat dissipation fan, thermostat, CPU heat dissipation fan, and power supplies dissipation fan, all for use with computers; cooling equipment for computers, namely, heat sinks; cooling kits in open or closed loops comprised primarily of internal cooling fans for computers; data fabric, namely, computer hardware and software that supports storage, processing, analysis and management of disparate data; data processing apparatus; data processors; data recording and storage media for use with computers, namely internal and external hard drives, solid state drives, blank USB devices, blank electronic storage media and blank digital storage media; devices for mobile and personal computing and communication, namely computer network interface devices, input devices for computers, chipsets for connecting multimedia home devices, home and VoIP phones and digital cordless phone devices, and wireless communication devices for voice data or image transmission; digital media streaming devices; downloadable and recorded computer programs for developing and controlling user interface; downloadable and recorded computer software development tools; downloadable and recorded software application programming interface (API), and software widget for machine learning, data mining, data guery, and data analysis downloadable computer firmware for the creation of software applications and application interfaces; downloadable computer software, namely, software development tools for the creation of software applications and application interfaces; downloadable open source software, namely, computer software for use in software development; downloadable software development kits; downloadable video game programs; dynamic random access memory (DRAM) controllers; dynamic random-access memory (DRAM); electronic card for image processing; electronic chips, namely electronic integrated circuit chips; electronic circuit boards; electronic circuit card; electronic circuits; electronic integrated circuits; electronic publications, downloadable, for use in the fields of operation and updating software for microprocessors, central processing units (CPUs) and graphics processing units (GPUs) and in the field of computer hardware and software for operation of video games; electronic sensors for use with microprocessor communication memory and data sensor telemetry; electronic storages being internal and external hard drives, solid state drives and blank USB devices, blank electronic storage media, and blank digital storage media; equipment for communication network, namely computer hardware for communicating audio, video and data between computers via a global computer network, wide-area computer networks, and peer-to-peer computer networks; graphics processing unit (GPU) cores; graphics accelerators; graphics and video processors; graphics cards; graphics processing unit (GPU) for artificial intelligence, cloud computing and high performance computing (HPC); graphics processing unit (GPU) for datacenter computers; graphics processing unit (GPU); graphics processor subsystem; graphics processors; graphics software; handheld computers; HD integrated graphics chips; headsets for virtual reality games; heat sinks for use in electronic components; integrated circuit cards being smart cards; integrated circuit chip sets; integrated circuit chips; integrated circuit memories, namely integrated circuit computer memories and integrated circuit semiconductor memories; integrated circuits and circuit boards for mobile and personal computing and communication; integrated circuits and circuit boards for use with computer networks and workstations, data recording and storage media for use with computers; integrated circuits, namely, circuits for computers communications, networking and programmable logic; integrated circuits, namely, graphics, video and multimedia integrated circuits; integrated circuits; interactive video games of virtual reality comprised of computer hardware for use with an external monitor and downloadable video game software; intercommunication apparatus, namely microprocessor data communication interconnect architecture responsible for collecting data and command control, interconnect architecture responsible for data sensor telemetry, system on a chip (SoC) architecture that connects die-to-die, chip-to-chip, and socket-to-socket, used across different microprocessors to enable increased computing performance, and network-on-a-chip technology that provides interfaces across microprocessor CPU and GPU cores, memory, hubs and data fabric to enable microprocessor communications; internal and external cooling fans for computers; Internet server; laptop computers; large scale

integrated circuits; logic circuits; blank magnetic data media; memory boards; memory cards for video game machines; memory expansion cards; memory expansion modules; memory for data processing apparatus, namely memories for data processing equipment; memory for use with computers, namely, computer memories; memory modules; memory, non-volatile memory and programmable logic devices, namely, blank flash memory cards, erasable programmable read-only memory (EPROMs), read-only memory (ROMs) and programmable logic microchips; microchips being computer hardware; microchips; microprocessor communication fabric, namely downloadable microprocessor operating firmware; microprocessor communication memory, namely random access memory (RAM) and video random access memory (VRAM) shared by a central processing unit (CPU) and graphics processing unit (GPU); microprocessor subsystems comprised of one or more microprocessors, central processing unit (CPU), CPU cores, and recorded and downloadable software for operating the foregoing; communication devices, namely, integrated circuits and circuit boards for telephones, wireless communication, radio and television transmission and reception; microprocessors; minicomputers; multimedia accelerator boards; multiprocessor chips; network servers; network-on-chip, namely microprocessor or hardware technology for use in scaling for high performance computing (HPC) and machine learning (ML) that provides for computer network interfaces across microprocessor central processing unit (CPU) and graphics processing unit (GPU) cores, computer memory, hubs and data fabric for enabling microprocessor communications and increasing computing performance and efficiency; operating system software; personal computers; personal digital assistants (PDAs); recorded and downloadable microprocessor operating software; semiconductor chips; semiconductors; semiconductor devices; semiconductor integrated circuits; semiconductor memories; semiconductor processors; semiconductor storage units, namely random access memory (RAM), dynamic random access memory (DRAM), synchronous dynamic random access memory (SDRAM), and video random access memory (VRAM); Smart cards being integrated circuit cards; SoC architecture, namely System on Chip (SoC) architecture for use with central processing units (CPU) and graphics processing units (GPU) that connects die-to-die, chip-to-chip, and socketto-socket, used across different microprocessors for enabling increased computing performance; downloadable computer and microprocessor software for operating all of the foregoing; software programmable microprocessors; solid state drives; storage cards being memory cards; video capture cards; video cards display cards; downloadable video game software; video graphics accelerator; video graphics processors; video servers; downloadable virtual reality game software; virtual reality glasses; virtual reality headsets; downloadable virtual reality software for creating realistic virtual reality experiences; virtual reality training simulation software in the field of realistic training simulation; wearable computers

International Class(es): 009 - Primary Class

U.S Class(es): 021, 023, 026, 036, 038

Class Status: ACTIVE

Basis: 1(b)

For: Computer chip and processor design services; computer hardware consulting; computer network management services, namely, monitoring of network systems for technical purposes; computer network monitoring services, namely, providing information on the operation of computer networks; computer programming; computer services, namely, providing technical information, programming, maintenance and updating of cloud-based design and development software tools and presentation layer component development software for building interfaces for computer software applications, Internet software applications, XML web services, and website software; computer software consultancy; computer software, hardware and firmware design services; consultancy and advisory services in respect of the design and development of data processors, computer chips and computer apparatus and equipment; consulting relating to development and research in the field of semiconductors, integrated circuits, memory devices, computer hardware and software; consulting relating to technical research in the field of semiconductors, integrated circuits, memory devices, computer hardware and software; consulting, designing, and new product development, in the field of

		ware and software; consulting, designing, new product development, information, all in the field of semiconductors, integrated circuits, memory
		grams and data, not physical conversion; data encryption and decoding
		n and implementation of computer software, computer hardware and
		igning semiconductors, integrated circuits, memory devices and computer
		, and consulting services relating to computer software and hardware; puter software, computer hardware, computer networks, computer firmware,
	-	Iter hardware and software; maintenance of computer software; memory
		rm as a service (PAAS) featuring computer software platforms for developing,
	sharing and accessing open source software, APIs, and graphical so	ftware; platform as a service (PAAS) featuring open source software
		from a wide variety of sources, data bases and files and of multiple data
		vsis; platform as a service (PAAS); platform as a service, namely, providing
		ns for developing and controlling user interfaces, software application
		ning, data query, and data analysis; providing software as a service, namely,
		plications and application interfaces; providing temporary use of online, non- er, transmission, reception, processing and digitizing of audio and video
		adable software for ensuring the security of computer networks, for access
		m data theft or damage by unauthorized users; providing temporary use of
	online, non-downloadable software for processing, storage, retrieval	transmission, display, input, output, compressing, decompressing,
		e of online, non-downloadable software for the receipt, display, and use of
	broadcast video, audio, and digital data signals; providing temporary	
		ble computer software for use in software development; research and
		uits, memory devices, computer hardware and software; scientific and omputer software, computer hardware, computer networks, computer
		AAS) featuring development tools for the creation of software applications
		-time transfer, transmission, reception, processing and digitizing of audio and
	video graphics information, software for ensuring the security of com	puter networks, for access control and security, and for use in protecting
		s, software for processing, storage, retrieval, transmission, display, input,
		rintout of data, software for the receipt, display, and use of broadcast video,
		nications, and software for use in software development; technical design
	consulting services in the field of computers and wireless computing diagnosing computer hardware and software problems; updating of a	
International Class(es):		U.S Class(es): 100, 101

Basis: 1(b)

Basis Information (Case Level)

Filed Use:	No	Currently Use:	No
Filed ITU:	Yes	Currently ITU:	Yes
Filed 44D:	Yes	Currently 44E:	No
Filed 44E:	No	Currently 66A:	No
Filed 66A:	No	Currently No Basis:	No
Filed No Basis:	No		
Current Owner(s) Info			
	Advanced Micro Devices, Inc.		
	2485 Augustine Drive Santa Clara, CALIFORNIA UNITED STATES 95054		
Legal Entity Type:	CORPORATION	State or Country Where Organized:	DELAWARE
Attorney/Correspond	ence Information		
Attorney of Record			
Attorney Name:	Belinda J. Scrimenti	Docket Number:	444-00-00976
Attorney Primary Email Address:	bscrimenti@pattishall.com	Attorney Email Authorized:	Yes
Correspondent			
Name/Address:	Belinda J. Scrimenti PATTISHALL, MCAULIFFE, NEWBURY, HILLIARD & GE 200 SOUTH WACKER DRIVE, SUITE 2900 CHICAGO, ILLINOIS UNITED STATES 60606	ERALDS	
Phone:	312-554-8935		
	bscrimenti@pattishall.com kep@pattishall.com sm@pattishall.com	Correspondent e-mail Authorized:	Yes
Domestic Representative - Not	Found		
Prosecution History			

Date		Description		Proceeding Number
Mar. 11, 2022		NOTIFICATION OF NON-FINAL A	CTION E-MAILED	6325
Mar. 11, 2022		NON-FINAL ACTION E-MAILED		6325
Mar. 11, 2022		NON-FINAL ACTION WRITTEN		93644
Feb. 02, 2022		ASSIGNED TO EXAMINER		93644
Jan. 19, 2022		TEAS/EMAIL CORRESPONDENC	CE ENTERED	88889
Jan. 18, 2022		CORRESPONDENCE RECEIVED	IN LAW OFFICE	88889
Jan. 18, 2022		TEAS RESPONSE TO OFFICE A	CTION RECEIVED	
Jul. 15, 2021		NOTIFICATION OF NON-FINAL A	CTION E-MAILED	6325
Jul. 15, 2021		NON-FINAL ACTION E-MAILED		6325
Jul. 15, 2021		NON-FINAL ACTION WRITTEN		93643
Jun. 16, 2021		ASSIGNED TO EXAMINER		93643
May 14, 2021		NEW APPLICATION OFFICE SUF	PPLIED DATA ENTERED IN	
Feb. 15, 2021		NEW APPLICATION ENTERED IN	N TRAM	
TM Staff and Location	n Information			
TM Attorney:	BATT, JAIME L		Law Office Assigned:	LAW OFFICE 125
File Location			-	
Current Location:	TMO LAW OFFICE 12 ASSIGNED	25 - EXAMINING ATTORNEY	Date in Location:	Mar. 11, 2022
Assignment Abstract	t Of Title Informa	ation - Click to Load		

Proceedings - Click to Load

EXHIBIT B-1

For assistance with TSDR, email <u>teas@uspto.gov</u> and include your serial number, the document you are looking for, and a screenshot of any error messages you have received.

Processing Wait Times: Please note that due to an extraordinary surge in applications, processing times are longer than usual. See <u>current trademark</u> <u>processing wait times</u> for more information.

STATUS	DOCUMENTS	MAINTENANCE			Back to Search		Print
	Generated on:	This page was generated by TSDR on 2022-07-28 06:0	4:32 EDT				
	Mark: II	NFINITY FABRIC					
					INFINI	ГY FA	BRIC
U	S Serial Number:	88022783	Application Filing Date:	Jul. 02, 2	2018		
US Regis	stration Number:	6486616	Registration Date:	Sep. 14,	2021		
Fi	iled as TEAS RF:	Yes	Currently TEAS RF:	Yes			
	Register:	Principal					
	Mark Type:	Trademark					
TM5	Common Status		LIVE/REGISTRATION/Issued and A	Active			
	Descriptor:		The trademark application has been	n register	red with the Office.		
	Statue	Registered. The registration date is used to determine v	when nost-registration maintenance	a documa	ints are due		
		Sep. 14, 2021					
F	Publication Date:		Notice of Allowance Date:	Jul 09 2	2019		
				oun 00, 2			
Mark Inf	ormation						
Mark I	Literal Elements:	INFINITY FABRIC					
Standard	Character Claim:	Yes. The mark consists of standard characters without of	claim to any particular font style, siz	ze, or col	or.		
Mai	rk Drawing Type:	4 - STANDARD CHARACTER MARK					Privacy - Terms

Disclaimer: "FABRIC"

Goods and Services

Note:

The following symbols indicate that the registrant/owner has amended the goods/services:

- Brackets [..] indicate deleted goods/services;
- Double parenthesis ((..)) identify any goods/services not claimed in a Section 15 affidavit of incontestability; and
- Asterisks *..* identify additional (new) wording in the goods/services.

For:	Semiconductor devices; semiconductor chips; semiconductors; computer hardware; microprocessor modules; microprocessor subsystems
	comprised of one or more microprocessors, central processing units (CPUs), CPU cores, and software for operating the foregoing; computer
	hardware subsystems comprised of microprocessor subsystems; computer subsystems comprised of computers; chipsets; integrated circuits;
	graphics processors; accelerated processors; graphics cards; video display cards; processor operating software; computer software for
	databasing, visualization, manipulation, and integration of information; computer graphics software; computer workstations, namely, computers
	designed for advanced technical or scientific applications; computer servers; microprocessor communication fabric, namely, data communication
	interconnect architecture responsible for collecting data, and command control interconnect architecture responsible for data sensor telemetry;
	data fabric, namely, hardware and software that supports storage, processing, analysis and management of disparate data; system on a chip
	(SOC) architecture for use with CPUs and GPUs, namely, SOC architecture that connects die-to-die, chip-to-chip, and socket-to-socket, used
	across different microprocessors to enable increased computing performance; network-on-a-chip, namely, technology that provides interfaces
	across microprocessor CPU and GPU cores, memory, hubs and data fabric to enable microprocessor communications and increase computing
	performance and efficiency; microprocessor communication memory and electronic sensors; graphics processor subsystem, namely,
	microprocessor subsystems comprised of one or more microprocessors, graphics processing units (GPUs), GPU cores, and software for
	operating the foregoing; graphics processing unit (GPU); GPU cores; dynamic random access memory (DRAM) controllers

International Class(es):	009 - Primary Class	U.S Class(es):	021, 023, 026, 036, 038
Class Status:	ACTIVE		
Basis:	1(a)		
First Use:	Dec. 31, 2019	Use in Commerce:	Feb. 01, 2020

Basis Information (Case Level)

Filed Use: N	No	Currently Use:	Yes	
Filed ITU: Y	Yes	Currently ITU:	No	
Filed 44D: N	No	Currently 44E:	No	
Filed 44E: N	No	Currently 66A:	No	

Filed 66A:	No	Currently No Basis:	No
Filed No Basis:	No		
Current Owner(s) Info	ormation		
Owner Name:	Advanced Micro Devices, Inc.		
Owner Address:	2485 Augustine Drive Santa Clara, CALIFORNIA UNITED STATES 95054		
Legal Entity Type:	CORPORATION	State or Country Where Organized:	
Attorney/Correspond	lence Information		
Attorney of Record			
Attorney Name:	Bradley L. Cohn	Docket Number:	444-928
Attorney Primary Email Address:	BLC@pattishall.com	Attorney Email Authorized:	Yes
Correspondent			
	Bradley L. Cohn PATTISHALL, MCAULIFFE, NEWBURY, HILLIARD & G 200 SOUTH WACKER DRIVE, SUITE 2900 C, ILLINOIS UNITED STATES 60606	ERALDS	
Phone:	312-554-8000	Fax:	(312) 554-8015
Correspondent e-mail:	BLC@pattishall.com BJS@pattishall.com kep@pattishall.com	Correspondent e-mail Authorized:	Yes
Domestic Representative - Not	Found		
Prosecution History			
Date	Description		Proceeding Number
Sep. 14, 2021	REGISTERED-PRINCIPAL REGISTER		
Aug. 13, 2021	NOTICE OF ACCEPTANCE OF STATEME	ENT OF USE E-MAILED	

Aug. 12, 2021	ALLOWED PRINCIPAL REGISTER - SOU ACCEPTED	
Jul. 21, 2021	STATEMENT OF USE PROCESSING COMPLETE	65362
Jul. 09, 2021	USE AMENDMENT FILED	65362
Jul. 21, 2021	CASE ASSIGNED TO INTENT TO USE PARALEGAL	65362
Jul. 09, 2021	TEAS STATEMENT OF USE RECEIVED	
Jan. 13, 2021	NOTICE OF APPROVAL OF EXTENSION REQUEST E-MAILED	
Jan. 11, 2021	SOU EXTENSION 3 GRANTED	98765
Jan. 11, 2021	SOU EXTENSION 3 FILED	98765
Jan. 11, 2021	TEAS EXTENSION RECEIVED	
Jul. 10, 2020	NOTICE OF APPROVAL OF EXTENSION REQUEST E-MAILED	
Jul. 08, 2020	SOU EXTENSION 2 GRANTED	98765
Jul. 08, 2020	SOU EXTENSION 2 FILED	98765
Jul. 08, 2020	TEAS EXTENSION RECEIVED	
Jan. 04, 2020	NOTICE OF APPROVAL OF EXTENSION REQUEST E-MAILED	
Jan. 02, 2020	SOU EXTENSION 1 GRANTED	98765
Jan. 02, 2020	SOU EXTENSION 1 FILED	98765
Jan. 02, 2020	TEAS EXTENSION RECEIVED	
Oct. 04, 2019	APPLICANT/CORRESPONDENCE CHANGES (NON-RESPONSIVE) ENTERED	88888
Oct. 04, 2019	TEAS CHANGE OF OWNER ADDRESS RECEIVED	
Jul. 09, 2019	NOA E-MAILED - SOU REQUIRED FROM APPLICANT	
May 14, 2019	OFFICIAL GAZETTE PUBLICATION CONFIRMATION E-MAILED	
May 14, 2019	PUBLISHED FOR OPPOSITION	
Apr. 24, 2019	NOTIFICATION OF NOTICE OF PUBLICATION E-MAILED	
Apr. 02, 2019	APPROVED FOR PUB - PRINCIPAL REGISTER	
Mar. 20, 2019	TEAS CHANGE OF CORRESPONDENCE RECEIVED	
Mar. 21, 2019	TEAS/EMAIL CORRESPONDENCE ENTERED	88889
Mar. 20, 2019	CORRESPONDENCE RECEIVED IN LAW OFFICE	88889
Mar. 20, 2019	TEAS RESPONSE TO OFFICE ACTION RECEIVED	
Sep. 20, 2018	NOTIFICATION OF NON-FINAL ACTION E-MAILED	6325

Sep. 20, 2018	NON-FINAL ACTION E-MAILED	6325
Sep. 20, 2018	NON-FINAL ACTION WRITTEN	73731
Sep. 20, 2018	ASSIGNED TO EXAMINER	73731
Sep. 05, 2018	PRELIMINARY/VOLUNTARY AMENDMENT - ENTERED	66213
Aug. 30, 2018	ASSIGNED TO LIE	66213
Aug. 14, 2018	TEAS VOLUNTARY AMENDMENT RECEIVED	
Jul. 07, 2018	NEW APPLICATION OFFICE SUPPLIED DATA ENTERED IN TRAM	
Jul. 05, 2018	NEW APPLICATION ENTERED IN TRAM	

TM Staff and Location Information

TM Staff Information - None

File Location

Current Location: PUBLICATION AND ISSUE SECTION

Date in Location: Aug. 12, 2021

Assignment Abstract Of Title Information - Click to Load

Proceedings - Click to Load



INFINITY FABRIC

Reg. No. 6,486,616 Registered Sep. 14, 2021 Int. Cl.: 9 Trademark Principal Register



Performing the Functions and Duties of the Under Secretary of Commerce for Intellectual Property and Director of the United States Patent and Trademark Office

Advanced Micro Devices, Inc. (DELAWARE CORPORATION) 2485 Augustine Drive Santa Clara, CALIFORNIA 95054

CLASS 9: Semiconductor devices; semiconductor chips; semiconductors; computer hardware; microprocessor modules; microprocessor subsystems comprised of one or more microprocessors, central processing units (CPUs), CPU cores, and software for operating the foregoing; computer hardware subsystems comprised of microprocessor subsystems; computer subsystems comprised of computers; chipsets; integrated circuits; graphics processors; accelerated processors; graphics cards; video display cards; processor operating software; computer software for databasing, visualization, manipulation, and integration of information; computer graphics software; computer workstations, namely, computers designed for advanced technical or scientific applications; computer servers; microprocessor communication fabric, namely, data communication interconnect architecture responsible for collecting data, and command control interconnect architecture responsible for data sensor telemetry; data fabric, namely, hardware and software that supports storage, processing, analysis and management of disparate data; system on a chip (SOC) architecture for use with CPUs and GPUs, namely, SOC architecture that connects die-to-die, chip-to-chip, and socketto-socket, used across different microprocessors to enable increased computing performance; network-on-a-chip, namely, technology that provides interfaces across microprocessor CPU and GPU cores, memory, hubs and data fabric to enable microprocessor communications and increase computing performance and efficiency; microprocessor communication memory and electronic sensors; graphics processor subsystem, namely, microprocessor subsystems comprised of one or more microprocessors, graphics processing units (GPUs), GPU cores, and software for operating the foregoing; graphics processing unit (GPU); GPU cores; dynamic random access memory (DRAM) controllers

FIRST USE 12-31-2019; IN COMMERCE 2-1-2020

THE MARK CONSISTS OF STANDARD CHARACTERS WITHOUT CLAIM TO ANY PARTICULAR FONT STYLE, SIZE OR COLOR

No claim is made to the exclusive right to use the following apart from the mark as shown: "FABRIC"

SER. NO. 88-022,783, FILED 07-02-2018



REQUIREMENTS TO MAINTAIN YOUR FEDERAL TRADEMARK REGISTRATION

WARNING: YOUR REGISTRATION WILL BE CANCELLED IF YOU DO NOT FILE THE DOCUMENTS BELOW DURING THE SPECIFIED TIME PERIODS.

Requirements in the First Ten Years* What and When to File:

- *First Filing Deadline:* You must file a Declaration of Use (or Excusable Nonuse) between the 5th and 6th years after the registration date. See 15 U.S.C. §§1058, 1141k. If the declaration is accepted, the registration will continue in force for the remainder of the ten-year period, calculated from the registration date, unless cancelled by an order of the Commissioner for Trademarks or a federal court.
- *Second Filing Deadline:* You must file a Declaration of Use (or Excusable Nonuse) and an Application for Renewal between the 9th and 10th years after the registration date.* See 15 U.S.C. §1059.

Requirements in Successive Ten-Year Periods* What and When to File:

• You must file a Declaration of Use (or Excusable Nonuse) and an Application for Renewal between every 9th and 10th-year period, calculated from the registration date.*

Grace Period Filings*

The above documents will be accepted as timely if filed within six months after the deadlines listed above with the payment of an additional fee.

*ATTENTION MADRID PROTOCOL REGISTRANTS: The holder of an international registration with an extension of protection to the United States under the Madrid Protocol must timely file the Declarations of Use (or Excusable Nonuse) referenced above directly with the United States Patent and Trademark Office (USPTO). The time periods for filing are based on the U.S. registration date (not the international registration date). The deadlines and grace periods for the Declarations of Use (or Excusable Nonuse) are identical to those for nationally issued registrations. See 15 U.S.C. §\$1058, 1141k. However, owners of international registrations do not file renewal applications at the USPTO. Instead, the holder must file a renewal of the underlying international registration at the International Bureau of the World Intellectual Property Organization, under Article 7 of the Madrid Protocol, before the expiration of each ten-year term of protection, calculated from the date of the international registration. See 15 U.S.C. §1141j. For more information and renewal forms for the international registration.

NOTE: Fees and requirements for maintaining registrations are subject to change. Please check the USPTO website for further information. With the exception of renewal applications for registered extensions of protection, you can file the registration maintenance documents referenced above online at http://www.uspto.gov.

NOTE: A courtesy e-mail reminder of USPTO maintenance filing deadlines will be sent to trademark owners/holders who authorize e-mail communication and maintain a current e-mail address with the USPTO. To ensure that e-mail is authorized and your address is current, please use the Trademark Electronic Application System (TEAS) Correspondence Address and Change of Owner Address Forms available at http://www.uspto.gov.

EXHIBIT B-2

For assistance with TSDR, email teas@uspto.gov and include your serial number, the document you are looking for, and a screenshot of any error messages you have received. Processing Wait Times: Please note that due to an extraordinary surge in applications, processing times are longer than usual. See current trademark processing wait times for more information. **Back to Search** Print STATUS DOCUMENTS Generated on: This page was generated by TSDR on 2022-07-28 06:06:47 EDT Mark: CDNA CDNA US Serial Number: 90200632 Application Filing Date: Sep. 22, 2020 **Register:** Principal Mark Type: Trademark **TM5 Common Status** LIVE/APPLICATION/Published for Opposition Descriptor: A pending trademark application has been examined by the Office and has been published in a way that provides an opportunity for the public to oppose its registration. Status: Notice of Allowance (NOA) sent (issued) to the applicant. Applicant must file a Statement of Use or Extension Request within six months of the NOA issuance date. Status Date: Mar. 01, 2022 Publication Date: Sep. 07, 2021 Notice of Allowance Date: Mar. 01, 2022 **Mark Information** Mark Literal Elements: CDNA Standard Character Claim: Yes. The mark consists of standard characters without claim to any particular font style, size, or color. Mark Drawing Type: 4 - STANDARD CHARACTER MARK Privacy - Terms Goods and Services

Note:

The following symbols indicate that the registrant/owner has amended the goods/services:

- Brackets [..] indicate deleted goods/services;
- Double parenthesis ((..)) identify any goods/services not claimed in a Section 15 affidavit of incontestability; and
- Asterisks *..* identify additional (new) wording in the goods/services.

For: Integrated circuits, namely, graphics, video and multimedia integrated circuits; integrated circuit chip sets; cards containing integrated circuits; integrated circuit chips; semiconductor devices; semiconductor chips; semiconductors; chipsets; computer hardware; microprocessors; microprocessor subsystems comprised of one or more microprocessors, central processing unit (CPU), CPU cores, and downloadable and recorded software for operating the foregoing; computer hardware subsystems comprised of microprocessor subsystems; microprocessor modules; system-on-a-chip (SOC) processors; system-on-a-chip (SoC) architecture for use with central processing units (CPU) and graphics processing units (GPU), namely, system-on-a-chip (SoC) architecture that connects die-to-die, chip-to-chip, and socket-to-socket, used across different microprocessors for enabling increased computing performance; system-on-a-chip (SoC) architecture for use with multiple graphics processing units (GPU), namely, system-on-a-chip (SoC) architecture that connects die-to-die, chip-to-chip, and socket-to-socket, used across multiple different microprocessors, optimized for machine learning and high-performance computing applications, to enhance GPU to GPU connectivity, to enable increased computing performance, to accelerate data center computer workloads, and to enable exascale-class supercomputers; network-on-a-chip, namely, microprocessor hardware technology for use in scaling for high performance computing (HPC) and machine learning (ML) that provides for computer network interfaces across microprocessor central processor unit (CPU) and graphics processing unit (GPU) cores, computer memory, hubs and data fabric for enabling microprocessor communications and increasing computing performance and efficiency; microprocessor communication fabric, namely, data communication interconnect architecture responsible for collecting data, and command control interconnect architecture responsible for data sensor telemetry; data fabric, namely, hardware and downloadable and recorded software that supports storage, processing, analysis and management of disparate data; graphics processors units (GPUs); video graphics processors units (GPUs); graphics processing units (GPU); GPU cores; graphics cards; video display cards; video capture cards; accelerated data processors; accelerated video processors; multimedia accelerator boards; computer accelerator board; graphics accelerators; video graphics accelerator; graphics processor subsystem, namely, microprocessor subsystems comprised of one or more microprocessors, graphics processing units (GPUs), GPU cores, and downloadable and recorded software for operating the foregoing; supercomputers; computer servers; network servers; computer workstations, namely, computers designed for advanced technical or scientific applications and high performance computing applications; digital media streaming devices; solid state drives; computer memory devices, namely, volatile memory devices; dynamic random-access memory (DRAM); dynamic random access memory (DRAM) controllers; downloadable and recorded software for operating all of the foregoing; downloadable and recorded computer software for computer graphics and video games; downloadable and recorded graphics software; downloadable and recorded video game software; downloadable and recorded computer software for operating computer and graphics processors; downloadable virtual reality game software; downloadable virtual reality software for creating realistic virtual reality experiences; downloadable and recorded computer software for databasing, visualization, manipulation, virtual reality immersion and integration of information; downloadable and recorded virtual reality training simulation software in the field of realistic training simulations; virtual reality headsets; virtual reality glasses; headsets for virtual reality games; downloadable interactive video games of virtual reality comprised of computer hardware for use with an external monitor and downloadable and recorded software; recorded interactive video games of virtual reality comprised of computer hardware for use with an external monitor and downloadable and recorded software

International Class(es): 009 - Primary Class

U.S Class(es): 021, 023, 026, 036, 038

Class Status: ACTIVE

Basis: 1(b) **Basis Information (Case Level)** Filed Use: No Currently Use: No Filed ITU: Yes Currently ITU: Yes Filed 44D: No Currently 44E: No Currently 66A: No Filed 44E: No Filed 66A: No Currently No Basis: No Filed No Basis: No **Current Owner(s) Information** Owner Name: Advanced Micro Devices. Inc. **Owner Address:** 2485 Augustine Drive Santa Clara, CALIFORNIA UNITED STATES 95054 Legal Entity Type: CORPORATION State or Country Where DELAWARE **Organized: Attorney/Correspondence Information** Attorney of Record Attorney Name: Belinda J. Scrimenti Docket Number: 444-983 Attorney Primary Email bscrimenti@pattishall.com Attorney Email Authorized: Yes Address: Correspondent Correspondent Belinda J. Scrimenti Name/Address: PATTISHALL, MCAULIFFE, NEWBURY, HILLIARD & GERALDS 200 SOUTH WACKER DRIVE, SUITE 2900 CHICAGO, ILLINOIS UNITED STATES 60606 Phone: 312-554-8000 **Fax:** (312) 554-8015 Correspondent e-mail: <u>bscrimenti@pattishall.com</u> kep@pattishall.com Correspondent e-mail Yes

Authorized:

Domestic Representative - Not Found

Prosecution History

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TM Staff and Location Information

TM Staff Information

TM Attorney: DELGIZZI, RONALD ERNES

Law Office Assigned: LAW OFFICE 107

Date in Location: Mar. 01, 2022

File Location

Current Location: INTENT TO USE SECTION

Assignment Abstract Of Title Information - Click to Load

Proceedings - Click to Load

EXHIBIT B-3

For assistance with TSDR, email <u>teas@uspto.gov</u> and include your serial number, the document you are looking for, and a screenshot of any error messages you have received.

Processing Wait Times: Please note that due to an extraordinary surge in applications, processing times are longer than usual. See <u>current trademark</u> <u>processing wait times</u> for more information.

STATUS	DOCUMENTS	MAINTENANCE		Back to Search	Print
	Generated on:	This page was generated by TSDR on 2022-07-28 06:0	05:55 EDT		
	Mark: F	RDNA			
				RI	DNA
U	S Serial Number:	88851130	Application Filing Date: Mai	r. 27, 2020	
US Regi	stration Number:	6598566	Registration Date: Dec	21, 2021	
	Register:	Principal			
	Mark Type:	Trademark			
ТМ5	Common Status Descriptor:		LIVE/REGISTRATION/Issued and Activ		
	Status:	Registered. The registration date is used to determine	when post-registration maintenance do	cuments are due.	
	Status Date:	Dec. 21, 2021			
I	Publication Date:	Sep. 01, 2020	Notice of Allowance Date: Oct	. 27, 2020	
Mark In	formation				
Mark	Literal Elements:	RDNA			
Standard	Character Claim:	Yes. The mark consists of standard characters without	claim to any particular font style, size, c	or color.	
Ма	rk Drawing Type:	4 - STANDARD CHARACTER MARK			
					Privacy - Terms

Trademark Status & Document Retrieval

Goods and Services

Note:

The following symbols indicate that the registrant/owner has amended the goods/services:

- Brackets [..] indicate deleted goods/services;
- Double parenthesis ((..)) identify any goods/services not claimed in a Section 15 affidavit of incontestability; and
- Asterisks *..* identify additional (new) wording in the goods/services.

For: Integrated circuits, namely, graphics, video and multimedia integrated circuits; cards containing integrated circuits; integrated circuit chips; semiconductor devices; semiconductor chips; semiconductors; computer hardware; graphics processors; video graphics processors; graphics processing unit (GPU); GPU cores; graphics cards; video cards; video display cards; video capture cards; accelerated data processors; accelerated video processors; multimedia accelerator boards; computer accelerator board; graphics accelerators; video graphics accelerator; graphics processor subsystem, namely, microprocessor subsystems comprised of one or more microprocessors, graphics processing units (GPUs), GPU cores; digital media streaming devices; headsets for virtual reality games

International Class(es):	009 - Primary Class	U.S Class(es):	021, 023, 026, 036, 038
Class Status:	ACTIVE		
Basis:	1(a)		
First Use:	Feb. 28, 2019	Use in Commerce:	Jul. 31, 2019
Basis Information (C	ase Level)		
Filed Use:	No	Currently Use:	Yes
Filed ITU:	Yes	Currently ITU:	No
Filed 44D:	No	Currently 44E:	No
Filed 44E:	No	Currently 66A:	No
Filed 66A:	No	Currently No Basis:	No
Filed No Basis:	No		

Current Owner(s) Information

Owner Name: Advanced Micro Devices, Inc.

Owner Address: 2485 Augustine Drive Santa Clara, CALIFORNIA UNITED STATES 95054

Legal Entity Type: CORPORATION

		Organized:	
Attorney/Correspond	lence Information		
Attorney of Record			
Attorney Name:	Belinda J. Scrimenti	Docket Number:	00444-00961
Attorney Primary Email Address:	bscrimenti@pattishall.com	Attorney Email Authorized:	Yes
Correspondent			
•	Belinda J. Scrimenti PATTISHALL, MCAULIFFE, NEWBURY, HILLIARD 200 SOUTH WACKER DRIVE, SUITE 2900 CHICAGO, ILLINOIS UNITED STATES 60606	& GERALDS	
Correspondent e-mail:	bscrimenti@pattishall.com kep@pattishall.com	Correspondent e-mail	
Domestic Representative - Not	<u>sm@pattishall.com</u> Found	Authorized:	
Domestic Representative - Not Prosecution History Date	Found		
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Apr. 02, 2020 NEW APPLICATION OFFICE SUPPLIED DATA ENTERED IN TRAM	Jun. 23, 2020	NON-FINAL ACTION WRITTEN	80797
TRAM	Jun. 15, 2020	ASSIGNED TO EXAMINER	80797
Mar. 31, 2020 NEW APPLICATION ENTERED IN TRAM	Apr. 02, 2020		
	Mar. 31, 2020	NEW APPLICATION ENTERED IN TRAM	

TM Staff and Location Information

TM Staff Information - None

File Location

Current Location: PUBLICATION AND ISSUE SECTION

Date in Location: Nov. 16, 2021

Assignment Abstract Of Title Information - Click to Load

https://tsdr.uspto.gov/#caseNumber=88851130&caseType=SERIAL_NO&searchType=statusSearch

Proceedings - Click to Load



RDNA

Reg. No. 6,598,566		
Registered Dec. 21, 2021		
Int. Cl.: 9		
Trademark		
Principal Register		

Advanced Micro Devices, Inc. (DELAWARE CORPORATION) 2485 Augustine Drive Santa Clara, CALIFORNIA 95054

CLASS 9: Integrated circuits, namely, graphics, video and multimedia integrated circuits; cards containing integrated circuits; integrated circuit chips; semiconductor devices; semiconductor chips; semiconductors; computer hardware; graphics processors; video graphics processors; graphics processing unit (GPU); GPU cores; graphics cards; video cards; video display cards; video capture cards; accelerated data processors; accelerated video processors; multimedia accelerator board; computer accelerator board; graphics accelerators; video graphics accelerator; graphics processor subsystem, namely, microprocessor subsystems comprised of one or more microprocessors, graphics processing units (GPUs), GPU cores; digital media streaming devices; headsets for virtual reality games

FIRST USE 2-28-2019; IN COMMERCE 7-31-2019

THE MARK CONSISTS OF STANDARD CHARACTERS WITHOUT CLAIM TO ANY PARTICULAR FONT STYLE, SIZE OR COLOR

SER. NO. 88-851,130, FILED 03-27-2020



Performing the Functions and Duties of the Under Secretary of Commerce for Intellectual Property and Director of the United States Patent and Trademark Office



REQUIREMENTS TO MAINTAIN YOUR FEDERAL TRADEMARK REGISTRATION

WARNING: YOUR REGISTRATION WILL BE CANCELLED IF YOU DO NOT FILE THE DOCUMENTS BELOW DURING THE SPECIFIED TIME PERIODS.

Requirements in the First Ten Years* What and When to File:

- *First Filing Deadline:* You must file a Declaration of Use (or Excusable Nonuse) between the 5th and 6th years after the registration date. See 15 U.S.C. §§1058, 1141k. If the declaration is accepted, the registration will continue in force for the remainder of the ten-year period, calculated from the registration date, unless cancelled by an order of the Commissioner for Trademarks or a federal court.
- *Second Filing Deadline:* You must file a Declaration of Use (or Excusable Nonuse) and an Application for Renewal between the 9th and 10th years after the registration date.* See 15 U.S.C. §1059.

Requirements in Successive Ten-Year Periods* What and When to File:

• You must file a Declaration of Use (or Excusable Nonuse) and an Application for Renewal between every 9th and 10th-year period, calculated from the registration date.*

Grace Period Filings*

The above documents will be accepted as timely if filed within six months after the deadlines listed above with the payment of an additional fee.

*ATTENTION MADRID PROTOCOL REGISTRANTS: The holder of an international registration with an extension of protection to the United States under the Madrid Protocol must timely file the Declarations of Use (or Excusable Nonuse) referenced above directly with the United States Patent and Trademark Office (USPTO). The time periods for filing are based on the U.S. registration date (not the international registration date). The deadlines and grace periods for the Declarations of Use (or Excusable Nonuse) are identical to those for nationally issued registrations. See 15 U.S.C. §\$1058, 1141k. However, owners of international registrations do not file renewal applications at the USPTO. Instead, the holder must file a renewal of the underlying international registration at the International Bureau of the World Intellectual Property Organization, under Article 7 of the Madrid Protocol, before the expiration of each ten-year term of protection, calculated from the date of the international registration. See 15 U.S.C. §1141j. For more information and renewal forms for the international registration.

NOTE: Fees and requirements for maintaining registrations are subject to change. Please check the USPTO website for further information. With the exception of renewal applications for registered extensions of protection, you can file the registration maintenance documents referenced above online at http://www.uspto.gov.

NOTE: A courtesy e-mail reminder of USPTO maintenance filing deadlines will be sent to trademark owners/holders who authorize e-mail communication and maintain a current e-mail address with the USPTO. To ensure that e-mail is authorized and your address is current, please use the Trademark Electronic Application System (TEAS) Correspondence Address and Change of Owner Address Forms available at http://www.uspto.gov.

EXHIBIT B-4

For assistance with TSDR messages you have receive	R , email <u>teas@uspto.gov</u> and include your serial r ed.	number, the document you are loo	oking for, and a screenshot of any erro	r
Processing Wait Times: P processing wait times for	Please note that due to an extraordinary surge in ap more information.	oplications, processing times are	longer than usual. See <u>current trader</u>	<u>nark</u>
STATUS DOCUMENTS	MAINTENANCE		Back to Search	Print
	This page was generated by TSDR on 2022-07-28 06	3:07:45 EDT		
Mark: \	1			,
US Serial Number:	88210086	Application Filing Date:	: Nov. 29, 2018	
US Registration Number:	6246103	Registration Date:	: Jan. 12, 2021	
Filed as TEAS RF:	Yes	Currently TEAS RF:	Yes	
Register:	Principal			
Mark Type:	Trademark			
TM5 Common Status		LIVE/REGISTRATION/Issued and	Active	
Descriptor:		The trademark application has be	en registered with the Office.	
Status:	Registered. The registration date is used to determine	e when post-registration maintenand	ce documents are due.	
	Jan. 12, 2021			
Publication Date:		Notice of Allowance Date:	: Jun. 02. 2020	
Mark Information				Privacy - Terms

Mark Literal Elements: V

Related Properties Information

Claimed Ownership of US <u>5361334</u>, <u>5581877</u> Registrations:

Goods and Services

Note:

The following symbols indicate that the registrant/owner has amended the goods/services:

- Brackets [..] indicate deleted goods/services;
- Double parenthesis ((..)) identify any goods/services not claimed in a Section 15 affidavit of incontestability; and
- Asterisks *..* identify additional (new) wording in the goods/services.

For: Integrated circuits, namely, graphics, video and multimedia integrated circuits; integrated circuit chip sets; cards containing integrated circuits; integrated circuit chips; semiconductor devices; semiconductor chips; semiconductors; chipsets; computer hardware; microprocessors; microprocessor subsystems comprised of one or more microprocessors, and downloadable and recorded software for operating the foregoing; computer hardware subsystems comprised of microprocessor subsystems; microprocessor modules; microprocessor communication fabric, namely, data communication interconnect architecture responsible for collecting data, and command control interconnect architecture responsible for data sensor telemetry; data fabric, namely, hardware and downloadable and recorded software that supports storage, processing, analysis and management of disparate data; graphics processors; video graphics processors; graphics processing unit (GPU); GPU cores; graphics cards; video cards; video display cards; video capture cards; accelerated data processors; accelerated video processors; multimedia accelerator boards; computer accelerator board; graphics accelerators; video graphics accelerator; graphics processor subsystem, namely, microprocessor subsystems comprised of one or more microprocessors, graphics processing units (GPUs), GPU cores, and downloadable and recorded software for operating the foregoing; computer servers; network servers; computer workstations, namely, computers designed for advanced technical or scientific applications and high performance computing applications; digital media streaming devices; downloadable and recorded software for operating all of the foregoing; downloadable and recorded computer software for computer graphics and video games; downloadable and recorded graphics software; downloadable and recorded video game software; downloadable and recorded computer software for operating computer and graphics processors; downloadable virtual reality game software; downloadable virtual reality software for creating realistic virtual reality experiences; downloadable and recorded computer software for databasing, visualization, manipulation, virtual reality immersion and integration of information; downloadable and recorded virtual reality training simulation software in the field of realistic training simulations; virtual reality headsets; virtual reality glasses; headsets for virtual reality games; interactive video games of virtual reality comprised of computer hardware for use with an external monitor and downloadable and recorded software

 International Class(es):
 009 - Primary Class
 U.S Class(es):
 021, 023, 026, 036, 038

 Class Status:
 ACTIVE
 Ia
 Ia
 Ia

First Use:	Feb. 2019	Use in Commerce:	Feb. 2019
Basis Information (Ca	ase Level)		
Filed Use:	No	Currently Use:	Yes
Filed ITU:	Yes	Currently ITU:	No
Filed 44D:	No	Currently 44E:	No
Filed 44E:	No	Currently 66A:	No
Filed 66A:	No	Currently No Basis:	No
Filed No Basis:	No		
Current Owner(s) Info	ormation		
Owner Name:	Advanced Micro Devices, Inc.		
Owner Address:	2485 Augustine Drive Santa Clara, CALIFORNIA UNITED STATES 95054		
Legal Entity Type:	CORPORATION	State or Country Where Organized:	DELAWARE
Attorney/Correspond	ence Information		
Attorney of Record			
Attorney Name:	Belinda J. Scrimenti	Docket Number:	444-929
Attorney Primary Email Address:	bscrimenti@pattishall.com	Attorney Email Authorized:	Yes
Correspondent			
	Belinda J. Scrimenti Pattishall, McAuliffe, Newbury, Hilliard 200 SOUTH WACKER DRIVE, SUITE 2900 CHICAGO, ILLINOIS UNITED STATES 60606		
Phone:	312-554-8000	Fax:	(312) 554-8015
Correspondent e-mail:	bscrimenti@pattishall.com kep@pattishall.com	Correspondent e-mail	Yes

Authorized:

Domestic Representative - Not Found

Prosecution History

Date	Description	Proceeding Number
Jan. 12, 2021	REGISTERED-PRINCIPAL REGISTER	
Dec. 11, 2020	NOTICE OF ACCEPTANCE OF STATEMENT OF USE E-MAILED	
Dec. 10, 2020	ALLOWED PRINCIPAL REGISTER - SOU ACCEPTED	
Dec. 10, 2020	STATEMENT OF USE PROCESSING COMPLETE	65362
Dec. 01, 2020	USE AMENDMENT FILED	65362
Dec. 10, 2020	CASE ASSIGNED TO INTENT TO USE PARALEGAL	65362
Dec. 01, 2020	TEAS STATEMENT OF USE RECEIVED	
Jun. 02, 2020	NOA E-MAILED - SOU REQUIRED FROM APPLICANT	
Apr. 07, 2020	OFFICIAL GAZETTE PUBLICATION CONFIRMATION E-MAILED	
Apr. 07, 2020	PUBLISHED FOR OPPOSITION	
Mar. 18, 2020	NOTIFICATION OF NOTICE OF PUBLICATION E-MAILED	
Mar. 02, 2020	APPROVED FOR PUB - PRINCIPAL REGISTER	
Mar. 02, 2020	EXAMINER'S AMENDMENT ENTERED	88888
Mar. 02, 2020	NOTIFICATION OF EXAMINERS AMENDMENT E-MAILED	6328
Mar. 02, 2020	EXAMINERS AMENDMENT E-MAILED	6328
Mar. 02, 2020	EXAMINERS AMENDMENT -WRITTEN	91164
Feb. 26, 2020	ASSIGNED TO EXAMINER	91164
Sep. 04, 2019	TEAS/EMAIL CORRESPONDENCE ENTERED	88889
Sep. 03, 2019	CORRESPONDENCE RECEIVED IN LAW OFFICE	88889
Sep. 03, 2019	TEAS RESPONSE TO OFFICE ACTION RECEIVED	
Mar. 01, 2019	NOTIFICATION OF NON-FINAL ACTION E-MAILED	6325
Mar. 01, 2019	NON-FINAL ACTION E-MAILED	6325

Mar. 01, 2019	NON-FINAL ACTION WRITTEN	91163
Mar. 01, 2019	ASSIGNED TO EXAMINER	91163
Dec. 18, 2018	NOTICE OF DESIGN SEARCH CODE E-MAILED	
Dec. 17, 2018	NEW APPLICATION OFFICE SUPPLIED DATA ENTERED IN TRAM	
Dec. 03, 2018	NEW APPLICATION ENTERED IN TRAM	

TM Staff and Location Information

TM Staff Information - None

File Location

Current Location: PUBLICATION AND ISSUE SECTION

Date in Location: Dec. 10, 2020

Assignment Abstract Of Title Information - Click to Load

Proceedings - Click to Load





Reg. No. 6,246,103 Registered Jan. 12, 2021 Int. Cl.: 9 Trademark Principal Register



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Director of the United States Patent and Trademark Office Advanced Micro Devices, Inc. (DELAWARE CORPORATION) 2485 Augustine Drive Santa Clara, CALIFORNIA 95054

CLASS 9: Integrated circuits, namely, graphics, video and multimedia integrated circuits; integrated circuit chip sets; cards containing integrated circuits; integrated circuit chips; semiconductor devices; semiconductor chips; semiconductors; chipsets; computer hardware; microprocessors; microprocessor subsystems comprised of one or more microprocessors, and downloadable and recorded software for operating the foregoing; computer hardware subsystems comprised of microprocessor subsystems; microprocessor modules; microprocessor communication fabric, namely, data communication interconnect architecture responsible for collecting data, and command control interconnect architecture responsible for data sensor telemetry; data fabric, namely, hardware and downloadable and recorded software that supports storage, processing, analysis and management of disparate data; graphics processors; video graphics processors; graphics processing unit (GPU); GPU cores; graphics cards; video cards; video display cards; video capture cards; accelerated data processors; accelerated video processors; multimedia accelerator boards; computer accelerator board; graphics accelerators; video graphics accelerator; graphics processor subsystem, namely, microprocessor subsystems comprised of one or more microprocessors, graphics processing units (GPUs), GPU cores, and downloadable and recorded software for operating the foregoing; computer servers; network servers; computer workstations, namely, computers designed for advanced technical or scientific applications and high performance computing applications; digital media streaming devices; downloadable and recorded software for operating all of the foregoing; downloadable and recorded computer software for computer graphics and video games; downloadable and recorded graphics software; downloadable and recorded video game software; downloadable and recorded computer software for operating computer and graphics processors; downloadable virtual reality game software; downloadable virtual reality software for creating realistic virtual reality experiences; downloadable and recorded computer software for databasing, visualization, manipulation, virtual reality immersion and



integration of information; downloadable and recorded virtual reality training simulation software in the field of realistic training simulations; virtual reality headsets; virtual reality glasses; headsets for virtual reality games; interactive video games of virtual reality comprised of computer hardware for use with an external monitor and downloadable and recorded software

FIRST USE 2-00-2019; IN COMMERCE 2-00-2019

The mark consists of two triangles, joined at one corner, so that a leg of each triangle appears to form the side of a letter "V". The left side of the design is a single dark triangle. The right side of the design is composed of a smaller triangle and four alternating light and dark horizontal stripes.

OWNER OF U.S. REG. NO. 5581877, 5361334

SER. NO. 88-210,086, FILED 11-29-2018

REQUIREMENTS TO MAINTAIN YOUR FEDERAL TRADEMARK REGISTRATION

WARNING: YOUR REGISTRATION WILL BE CANCELLED IF YOU DO NOT FILE THE DOCUMENTS BELOW DURING THE SPECIFIED TIME PERIODS.

Requirements in the First Ten Years* What and When to File:

- *First Filing Deadline:* You must file a Declaration of Use (or Excusable Nonuse) between the 5th and 6th years after the registration date. See 15 U.S.C. §§1058, 1141k. If the declaration is accepted, the registration will continue in force for the remainder of the ten-year period, calculated from the registration date, unless cancelled by an order of the Commissioner for Trademarks or a federal court.
- *Second Filing Deadline:* You must file a Declaration of Use (or Excusable Nonuse) and an Application for Renewal between the 9th and 10th years after the registration date.* See 15 U.S.C. §1059.

Requirements in Successive Ten-Year Periods* What and When to File:

• You must file a Declaration of Use (or Excusable Nonuse) and an Application for Renewal between every 9th and 10th-year period, calculated from the registration date.*

Grace Period Filings*

The above documents will be accepted as timely if filed within six months after the deadlines listed above with the payment of an additional fee.

*ATTENTION MADRID PROTOCOL REGISTRANTS: The holder of an international registration with an extension of protection to the United States under the Madrid Protocol must timely file the Declarations of Use (or Excusable Nonuse) referenced above directly with the United States Patent and Trademark Office (USPTO). The time periods for filing are based on the U.S. registration date (not the international registration date). The deadlines and grace periods for the Declarations of Use (or Excusable Nonuse) are identical to those for nationally issued registrations. See 15 U.S.C. §\$1058, 1141k. However, owners of international registrations do not file renewal applications at the USPTO. Instead, the holder must file a renewal of the underlying international registration at the International Bureau of the World Intellectual Property Organization, under Article 7 of the Madrid Protocol, before the expiration of each ten-year term of protection, calculated from the date of the international registration. See 15 U.S.C. §1141j. For more information and renewal forms for the international registration.

NOTE: Fees and requirements for maintaining registrations are subject to change. Please check the USPTO website for further information. With the exception of renewal applications for registered extensions of protection, you can file the registration maintenance documents referenced above online at http://www.uspto.gov.

NOTE: A courtesy e-mail reminder of USPTO maintenance filing deadlines will be sent to trademark owners/holders who authorize e-mail communication and maintain a current e-mail address with the USPTO. To ensure that e-mail is authorized and your address is current, please use the Trademark Electronic Application System (TEAS) Correspondence Address and Change of Owner Address Forms available at http://www.uspto.gov.