

Custom ASIC & FPGA Development

Diverse Expertise

Mil-Aero and Commercial Appl.

SoC Design Avionics DSP Rad Hard Design / Layout Cryptographic Algorithms ARM Core Design and Integration Datacom / Telecom Satellite Communications Processor Design Memory Design Ethernet, SRIO, PCI, many others *Ultra-High Reliability*



Solutions & Services

- Definition Architecture Specifications
- System Level Design, C- Level Modeling and C to RTL
- Verilog and VHDL RTL
- Transistor Level Design: Analog, Mixed Signal and IO
- Advanced Verification: System Verilog, Vera, Specman
- Design for Test: Scan, BIST, JTAG, ATPG
- Physical Design: P&R, full custom, rad hard
- Software and Firmware Development
- Prototyping and Debug

Verification Quickramp Program

• Pre-packaged System Verilog Test Environment, test components and training

Over 200 Successful Developments





Custom ASIC & FPGA Development Since 1993

FirstPass specializes in providing customers with comprehensive IC design services of the highest quality. We excel in supporting you and solving your complex IC challenges. We provide complete turnkey design or can supplement your existing design team. Through our rigorous high quality engineering practices, flexible and rapid staffing, advanced EDA tools and technical expertise, our highly responsive IC design team partners with you to successfully achieve your project goals.

FirstPass is uniquely qualified to provide every phase of FPGA or ASIC development; including architecture definition, specification development, analog/digital/mixed signal design, verification, place and route, and physical verification – through tapeout and transfer to production.

Our engineering team averages over 20 years of experience in the semiconductor industry across diverse market sectors. Armed with state-of-the-art equipment and design tools, our specialists can support your most critical and demanding projects through any stage of the entire development process, by implementing our adaptive engineering strategies and comprehensive design methodologies.

History

FirstPass incorporated in 1993 with the goal of providing ASIC design services to the Military/Aerospace marketplace. We soon expanded into commercial markets and our customer base grew. From 2000 to 2005, we were a captive design house for Vitesse Semiconductor Corp., focusing on advanced development of complex ASICs for the telecom market including Intelligent Switch Fabrics, PCI, and SATA type devices – and aggressively driving methodology improvements in all phases of design.

In 2005, FirstPass re-incorporated as an independent custom design services company. We have enjoyed strong growth by firmly re-establishing ourselves as a preferred provider of ASIC and FPGA design services; specializing in comprehensive IC engineering development methodologies that support any scope of project complexity, schedules, and cost constraints. We remain ahead of the technology curve by maintaining our strict and rigorous quality control standards, expanding our engineering expertise, and constantly incorporating the technical changes and advances in fabrication processes, tools, and methodologies. FirstPass offices are in Castle Rock, CO and Tempe, AZ.

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