

03-09-2001

FORM PTO-1-31-92

RECC



U.S. DEPARTMENT OF COMMERCE Patent and Trademark Office

101631332

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):
Silicon Metrics Corporation

Individual(s) Association
 General Partnership Limited Partnership
 Corporation-State
 Other LLC

Additional name(s) of conveying party(ies) attached? Yes No

3. Nature of conveyance:
 Assignment Merger
 Security Agreement Change of Name
 Other

Execution Date: 12/3/00

2. Name and address of receiving party(ies):
Name: Silicon Valley Bank
Internal Address: Loan Documentation HA155
Street Address: 3003 Tasman Drive
City: Santa Clara State: Ca ZIP: 95054

Individual(s) Citizenship
 Association
 General Partnership
 Limited Partnership
 Corporation-State
 Other

If assignee is not domiciled in the United States, a domestic representative designation is attached Yes No
(Designations must be a separate document from assignment)

Additional name(s) & address(es) attached? Yes No

4. Application number(s) or trademark number(s):

A. Trademark Application No.(s)
75/681,580 75/705,887
75/584,149 75/705,794
75/936,236 75/517,317
75/705,795 75/452,232
75/727,112
75/610,439

B. Trademark Registration No.(s)

Additional numbers attached? Yes No

5. Name and address of party to whom correspondence concerning document should be mailed:
Name: Silicon Valley Bank
Internal Address: Loan Documentation HA155
Street Address: 3003 Tasman Dr.
City: Santa Clara State: Ca ZIP: 95054

6. Total number of applications and registrations involved: 10

7. Total fee (37 CFR 3.41):\$ 265.00
 Enclosed
 Authorized to be charged to deposit account

8. Deposit account number:
(Attach duplicate copy of this page if paying by deposit account)

DO NOT USE THIS SPACE

03/08/2001 DBYRNE 00000211 75681580

01 FC: 481 40.00 DP
02 FC: 482 225.00 DP

265E

9. Statement and signature.
To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Shannon Hubbard
Name of Person Signing

Shannon Hubbard
Signature

2/20/01
Date

Total number of pages comprising cover sheet: 5

OMB No 0651-0011 (exp 4/94)

ADDENDUM TO INTELLECTUAL PROPERTY SECURITY AGREEMENT

This Addendum to Intellectual Property Security Agreement is executed pursuant to, and is an addendum to, an Intellectual Property Security Agreement, dated December 3, 1999, by and between Silicon Metrics Corporation ("Grantor") with its principal office at 12710 Research Blvd., Suite 300, Austin, Texas 78759, and Silicon Valley Bank ("Bank") with its principal office at 3003 Tasman Drive, Santa Clara, CA 95054. This Addendum to Intellectual Property Security Agreement is presented for recordation as constructive notice that Grantor, the owner of the intellectual property identified in the exhibit(s) attached hereto, has granted to Bank a security interest in the intellectual property described on exhibits(s) attached hereto, and the exclusive rights comprised in the intellectual property, to secure payment of a debt.

IN WITNESS WHEREOF, Grantor has executed this Addendum to Intellectual Property Security Agreement as of February 12, 2001.

GRANTOR:

SILICON METRICS CORPORATION

By: Scott Gore
Name: SCOTT GORE
Title: CFO

EXHIBIT "A"
COPYRIGHTS

SCHEDULE A - ISSUED COPYRIGHTS

<u>COPYRIGHT DESCRIPTION</u>	<u>REGISTRATION NUMBER</u>	<u>DATE OF ISSUANCE</u>
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— NONE —

SCHEDULE B - PENDING COPYRIGHT APPLICATIONS

<u>COPYRIGHT DESCRIPTION</u>	<u>APPLICATION NUMBER</u>	<u>DATE OF FILING</u>	<u>DATE OF CREATION</u>	<u>FIRST DATE OF PUBLIC DISTRIBUTION</u>
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— NONE —

SCHEDULE C - UNREGISTERED COPYRIGHTS (Where No Copyright Application is Pending)

<u>DESCRIPTION</u>	<u>CREATION</u>	<u>DISTRIBUTION FROM ASSIGNOR</u>	<u>ASSIGNOR</u>
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Exhibit "B" attached to that certain Addendum to Intellectual Property Security Agreement dated February 12, 2001.

EXHIBIT "B"
PATENTS

<u>PATENT DESCRIPTION</u>	<u>REGISTRATION NUMBER</u>	<u>DATE OF ISSUANCE</u>
Method and System for Creating Electronic Circuitry	09/090,457	06/04/1998
Application Personality	09/768,037	01/22/2001
Method for Highly Accurate Black Box Power Dissipation	Applied for	
Interconnect Model Compiler	09/707,757	11/07/2000
Plug-ins	09/707,333	11/07/2000
Encrypted Compiler	Applied for	

EXHIBIT "C"
TRADEMARKS

<u>TRADEMARK DESCRIPTION</u>	<u>COUNTRY</u>	<u>SERIAL NO.</u>	<u>REG. NO</u>	<u>STATUS</u>
Silicon Metrics and Design	US	75-681,580	2387329	Registered
Silicon Metrics	US	75-584,149	2353345	Registered
SiliconSmart	US	75-936,236		Published
CoreRater	US	75-610,438		Abandoned
IORater	US	75-517,319		Abandoned
SpicePilot	US	75-517,318		Abandoned
PCX	US	75-705,887		Allowed
BlockRater	US	75-705,794		Pending
PowerRater	US	75-517,317		Pending
MemRater	US	75-705,795		Pending
MacroRater	US	75-727,112		Pending
RealityCheck	US	75-610,439		Published
CellRater	US	75-452,232		In process