

10-20-1998

FORM PTO-1594

R

(Rev. 6-93)

10-14-98



ET

U.S. DEPARTMENT OF COMMERCE

Patent and Trademark Office

100856143

To the Honorable Commissioner of Patents :

original documents or copy thereof.

MRD 10-14-98

1. Name of conveying party(ies):
 Credit Lyonnais, acting through its New York branch, as agent

Individual(s) Association
 General Partnership Limited Partnership
 Corporation-State
 Other Licensed New York branch of French Bank
 Additional name(s) of conveying party(ies) attached? Yes
 No

3. Nature of conveyance:
 Assignment Merger
 Security Agreement Change of Name
 Other Release of Trademark Security Interest

Execution Date: April 11, 1996

2. Name and address of receiving party(ies):
 Name: Cadence Design Systems, Inc.

Internal Address:
 Internal Address:
 Street Address: 555 River Oaks Parkway
 City: San Jose State: CA Zip: 95134

Individual(s) citizenship _____
 Association _____
 General Partnership _____
 Limited Partnership _____
 Corporation-State Delaware
 Other _____

If assignee is not domiciled in the United States, a domestic representative designation is attached: Yes No
 (Designation must be a separate document from Assignment).
 Additional name(s) & address(es) attached? Yes No

4. Application number(s) or registration number(s):
 A. Trademark Application No.(s)
Please see attached

Additional numbers attached? Yes No

B. Trademark Registration No.(s)
Please see attached

5. Name and address of party to whom correspondence concerning document should be mailed:
 Name: Cooley Godward LLP
 Internal Address: Attn: Diana R. Sanchez
 Street Address: 5 Palo Alto Square
 City: Palo Alto State: CA ZIP 94306

6. Total number of applications and registration involved: 71

7. Total fee (37 CFR 3.41):..... \$1740.00
 Enclosed
 Authorized to be charged to deposit account

8. Deposit account number:
 (Attach duplicate copy of this page if paying by deposit account)

10/19/1998 JSHABAZZ 00000090 75042237

DO NOT USE THIS SPACE

01 F1:48 40.00 DP
 02 F2:48 statement and signature. 1750.00 DP

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Diana R. Sanchez 10/5/98
 Diana R. Sanchez Date

Total number of pages including cover sheet, attachments, and document: 14

Mail documents to be recorded with required cover sheet information to:
 Commissioner of Patents and Trademarks, Box Assignments, Washington, D.C. 20231

TRADEMARKS

1. FLEXCHIP

STATUS: Pending; Preliminary Drawing Page Data.

SERIAL NO.: 75-042,237

2. CONVERGENCE

STATUS: Pending; New Application - Record Initialized Not Assigned to Examiner; Intent to Use - Application; Intent To Use - Current.

SERIAL NO.: 75-025,000

3. PACKAGER-XL

STATUS: Pending; New Application - Record Initialized Not Assigned to Examiner; Intent to Use - Application; Intent To Use - Current.

SERIAL NO.: 75-018,648

4. SILICON WORKS

STATUS: Pending; New Application - Record Initialized Not Assigned to Examiner; Intent to Use - Application; Intent To Use - Current.

SERIAL NO.: 75-018,647

5. SILICON ENSEMBLE

STATUS: Pending; New Application - Record Initialized Not Assigned to Examiner; Preliminary Drawing Page Data

SERIAL NO.: 75-018,646

6. SILICON SYNTHESIS

STATUS: Pending; New Application - Record Initialized Not Assigned to Examiner; Intent to Use - Application; Intent To Use - Current.

SERIAL NO.: 75-018,644

7. **TELOS VENTURE PARTNERS**

STATUS: Pending; New Application - Record Initialized Not Assigned to Examiner; Intent to Use - Application; Intent To Use - Current.

SERIAL NO.: 75-017,952

8. **SOURCELINK**

STATUS: Pending; New Application - Record Initialized Not Assigned to Examiner.

SERIAL NO.: 75-017,086

9. **SMARTBLOCKS**

STATUS: Pending; New Application - Record Initialized Not Assigned to Examiner; Intent to Use - Application; Intent To Use - Current.

SERIAL NO.: 75-013,935

10. **DESIGN FRAMEWORK II**

STATUS: Pending; Non-Final Action - Mailed

SERIAL NO.: 75-005,216

11. **SILICONQUEST**

STATUS: Pending; Non-Final Action - Mailed; Intent to Use - Application; Intent To Use - Current.

SERIAL NO.: 75-003,841

12. **LOGIC WORKBENCH**

STATUS: Pending; Non-Final Action - Mailed

SERIAL NO.: 75-003,827

13. MPS

STATUS: Pending; Non-Final Action - Mailed; Intent to Use - Application; Intent To Use - Current

SERIAL NO.: 74-725,578

14. CELL3 ENSEMBLE

STATUS: Pending; Non-Final Action - Mailed

SERIAL NO.: 74-725,577

15. CONNECTIONS:

STATUS: Pending; Non-Final Action - Mailed

SERIAL NO.: 74-725,576

16. CELL ENSEMBLE:

STATUS: Pending; Non-Final Action - Mailed.

SERIAL NO.: 74-725,575

17. SKILL:

STATUS: Pending; Non-Final Action - Mailed

SERIAL NO.: 74-722,798

18. GATE ENSEMBLE:

STATUS: Pending; Non-Final Action - Mailed

SERIAL NO.: 74-722,386

19. CADENCE:

STATUS: Pending; Non-Final Action - Mailed

SERIAL NO.: 74-719,329

20. **CONCEPT:**

STATUS: Pending; Non-Final Action - Mailed

SERIAL NO.: 74-715,869

21. **SPECTRE:**

STATUS: Pending; Non-Final Action - Mailed

SERIAL NO.: 74-703,402

22. **LEAPFROG:**

STATUS: Pending; Suspension Letter - Mailed

SERIAL NO.: 74-693,214

23. **VIRTUOSO:**

STATUS: Pending; Publication/Registration Review Complete

SERIAL NO.: 74-693,213

24. **ANALOG ARTIST**

STATUS: Pending; Non-Final Action - Mailed

SERIAL NO.: 74-691,850

25. **COMPOSER:**

STATUS: Pending; Non-Final Action - Mailed

SERIAL NO.: 74-691,580

26. **ALLEGRO:**

STATUS: Pending; Non-Final Action - Mailed

SERIAL NO.: 74-691,476

27. CADENCE:

STATUS: Pending; Non-Final Action - Mailed

SERIAL NO.: 74-689,105

28. SIG-XPLORER:

STATUS: Pending-Published for Opposition;
Publication/Registration Review Complete; Intent to Use -
Application; Intent To Use - Current

SERIAL NO.: 74-668,835

29. BOARDQUEST:

STATUS: Pending-Published for Opposition;
Publication/Registration Review Complete; Intent to Use -
Application; Intent To Use - Current

SERIAL NO.: 74-652,558

30. INCA:

STATUS: Pending-Published for Opposition;
Publication/Registration Review Complete; Intent to Use -
Application; Intent To Use - Current

SERIAL NO.: 74-652,113

31. NC-VERILOG:

STATUS: Pending-Published for Opposition;
Publication/Registration Review Complete; Intent to Use -
Application; Intent To Use - Current

SERIAL NO.: 74-652,112

32. **FPGA DESIGNER:**

STATUS: Pending; Non-Final Action - Mailed; Intent to Use - Application; Intent To Use - Current

SERIAL NO.: 74-648,026

33. **PLD DESIGNER:**

STATUS: Pending; Non-Final Action - Mailed; Intent to Use - Application; Intent To Use - Current

SERIAL NO.: 74-647,952

34. **SYSTEMS & NETWORKS:**

STATUS: Pending; Non-Final Action - Mailed; Intent to Use - Application; Intent To Use - Current

SERIAL NO.: 74-647,939

35. **PREVAIL:**

STATUS: Pending-Published for Opposition; Publication/Registration Review Complete; Intent to Use - Application; Intent To Use - Current

SERIAL NO.: 74-620,098

36. **VAMPIRE:**

STATUS: Pending-Published for Opposition; Notice of Allowance - Issued; Intent to Use - Application; Intent To Use - Current

SERIAL NO.: 74-550,196

37. **CHECKPLUS:**

STATUS: Pending-Published for Opposition; Notice of Allowance - Issued; Intent to Use - Application; Intent To Use - Current

SERIAL NO.: 74-521,217

38. ALTA GROUP:

STATUS: Registered; Intent to Use - Application

SERIAL NO.: 74-507,272

39. SUMMIT GROUP OF CADENCE DESIGN SYSTEMS INC.:

STATUS: Abandoned - Failure To Respond; Intent to Use -
Application; Intent To Use - Current

SERIAL NO.: 74-485,164

40. SUMMIT GROUP:

STATUS: Abandoned - Failure To Respond; Intent to Use -
Application; Intent To Use - Current

SERIAL NO.: 74-485,163

41. OPENBOOK:

STATUS: Registered

SERIAL NO.: 74-455,671

42. DSPARTNERS:

STATUS: Registered

SERIAL NO.: 74-344,675

43. BONES:

STATUS: Registered

SERIAL NO.: 74-303,092

44. SYNERGY:

STATUS: Pending; Report Completed Suspension Check - Case Still
Suspended; Intent to Use - Application; Intent To Use - Current

SERIAL NO.: 74-280,284

45. BONES PLANNET:

STATUS: Registered

SERIAL NO.: 74-247,454

46. BONES DESIGNER:

STATUS: Registered

SERIAL NO.: 74-247,453

47. BONES:

STATUS: Registered

SERIAL NO.: 74-055,085

48. VERITOOLS:

STATUS: Abandoned - No Statement of Use Filed; Intent to Use -
Application; Intent To Use - Current

SERIAL NO.: 74-039,235

49. TEMPO:

STATUS: Abandoned - No Statement of Use Filed; Intent to Use -
Application; Intent To Use - Current

SERIAL NO.: 74-007,974

50. SERENADE:

STATUS: Abandoned - Failure To Respond; Intent to Use -
Application; Intent To Use - Current

SERIAL NO.: 74-007,973

51. DIVA:

STATUS: Registered; Statement of Use - Registration Review
Complete; Intent to Use - Application

SERIAL NO.: 74-007,972

52. OVERTURE:

STATUS: Abandoned - No Statement of Use Filed; Intent to Use -
Application; Intent To Use - Current

SERIAL NO.: 74-007,968

53. VIVACE:

STATUS: Abandoned - No Statement of Use Filed; Intent to Use -
Application; Intent To Use - Current

SERIAL NO.: 74-007,061

54. VERIFault-XL

STATUS: Registered

SERIAL NO.: 73-784,162

55. THERMAX

STATUS: Registered

SERIAL NO.: 73-714,915

56. SYMBAD

STATUS: Canceled - Section 8

SERIAL NO.: 73-663,799

57. TESTGRADE

STATUS: Canceled - Section 8

SERIAL NO.: 73-617,372

58. OMNICARDS

STATUS: Canceled - Section 8

SERIAL NO.: 73-612,126

59. OMNIPRO

STATUS: Canceled - Section 8

SERIAL NO.: 73-612,125

60. OMNIPRO PC

STATUS: Canceled - Section 8

SERIAL NO.: 73-612,124

61. OMNIRROUTE

STATUS: Canceled - Section 8

SERIAL NO.: 73-612,122

62. BITGRADE

STATUS: Canceled - Section 8

SERIAL NO.: 73-610,110

63. VHDL

STATUS: Registered; Section 8 & 15 - Accepted & Acknowledged

SERIAL NO.: 73-610,107

64. TESTSCAN

STATUS: Canceled - Section 8

SERIAL NO.: 73-602,994

65. VERILOG

STATUS: Registered

SERIAL NO.: 73-595,361

66. LAYDE

STATUS: Registered; Section 8 & 15 - Accepted & Acknowledged

SERIAL NO.: 73-575,238

67. DRACULA

STATUS: Registered; Section 8 & 15 - Accepted & Acknowledged

SERIAL NO.: 73-575,237

68. LOGCAP

STATUS: Canceled - Section 8

SERIAL NO.: 73-427,639

69. SOUTHERN IRON WORKS, INC.

STATUS: Canceled - Section 8

SERIAL NO.: 73-328,783

70. ASI

STATUS: Registered; Section 8 & 15 - Accepted & Acknowledged

SERIAL NO.: 73-299,065

71. MASKAP

STATUS: Registered; Section 8 & 15 - Accepted & Acknowledged

SERIAL NO.: 73-284,751

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Registrant/Applicant: Cadence Design Systems, Inc.
Trademarks: See Attached Exhibit A

RELEASE OF TRADEMARK SECURITY INTEREST

WHEREAS, Cadence Design Systems, Inc., a Delaware corporation, has an interest in the titles listed in the attached Exhibit A, now registered or in the process of registration in the United States Patent and Trademark Office; and

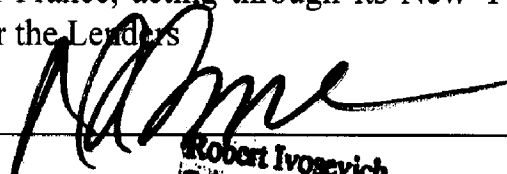
WHEREAS, Cadence Design Systems, Inc. entered into an Intellectual Property Assignment dated as of April 11, 1996 (the "Assignment"), with Credit Lyonnais, a bank organized under the laws of the Republic of France, acting through its New York Branch, as Agent for the Lenders (hereinafter "Lender"); a true and correct copy of which was recorded by the United States Patent and Trademark Office on April 11, 1996;

NOW, THEREFORE, for good and valuable consideration, receipt of which is hereby acknowledged, Lender hereby:

1. Represents and warrants that it has not sold, transferred, assigned, pledged or granted a security interest in, or encumbered its rights under the Assignment;
2. Releases and reassigns to Cadence Design Systems, Inc. any and all liens, security interests, right, title and interest of Lender pursuant to the Assignment in the Trademarks and Trademark applications which are described more fully on Exhibit A; and
3. Authorizes and requests the Commissioner of Patents and Trademarks of the United States of America to note and record the existence of the release hereby given.

IN WITNESS WHEREOF, Lender has caused this Release to be signed by its duly authorized representative as of this 30th day of September, 1998.

Credit Lyonnais, a bank organized under the laws of the Republic of France, acting through its New York Branch, as Agent for the Lenders

By: 
 Name: Robert Ivosevich
 Title: Senior Vice President